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RALPH E. LOCHER	(Depositor's name)
REG. NO. 41,947	(Signature)
May 16, 2006	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/623,843	07/21/2003	Veronika Polei	P2002,0618	6526

TITLE OF INVENTION: METHOD FOR FABRICATING A BURIED BIT LINE FOR A SEMICONDUCTOR MEMORY

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$1700	05/16/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, KHIEM D	2823	438-262000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

☐ Change of correspondence address (or Change of Correspondence Address form PTO/SB/122) attached.

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1 Laurence A. Greenberg

2 Werner H. Stemer

3 Ralph E. Locher

3. ASSIGNEE NAME AND RESIDENCE DATA TO BE PRINTED ON THE PATENT (print or type)

PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

(A) NAME OF ASSIGNEE

Infineon Technologies AG

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Muenchen, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27.

☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(g)(2).

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RALPH E. LOCHER
REG. NO. 41,947

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RALPH E. LOCHER
REG. NO. 41,947

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